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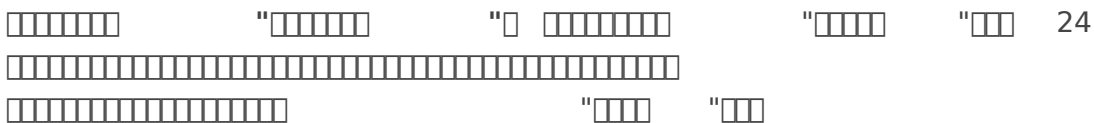
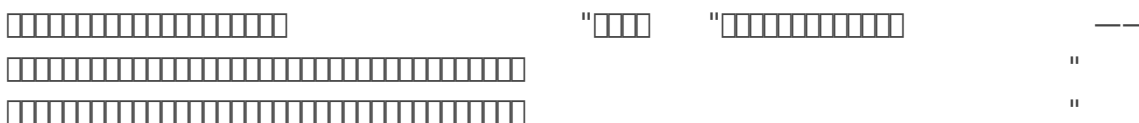
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The diagram illustrates a 16-bit bus system. It features four horizontal data buses, each composed of four adjacent square segments, representing 4-bit buses. These are labeled at the top as "Data Bus 0", "Data Bus 1", "Data Bus 2", and "Data Bus 3". Below these is a single, long horizontal address bus, also composed of 16 square segments, labeled "Address Bus". At the bottom, there are four more horizontal data buses, each composed of four adjacent square segments, representing 4-bit buses. These are labeled at the bottom as "Data Bus 4", "Data Bus 5", "Data Bus 6", and "Data Bus 7".

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Diagram illustrating a sequence of segments. The top row shows a long bar divided into 20 equal segments. The bottom row shows a shorter bar divided into 10 equal segments, followed by a dashed line and a single segment, and then two more segments, each preceded by a double quote symbol.

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A horizontal number line with arrows at both ends. It is marked with numbers from 0 to 100 in increments of 10 (0, 10, 20, 30, 40, 50, 60, 70, 80, 90, 100). A blue arrow starts at 0 and points to 10.

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Diagram illustrating a 1D lattice with $L=100$ sites. The lattice is represented by a horizontal chain of 100 squares. The first 10 squares are filled with red dots, representing occupied sites. The remaining 90 squares are empty, representing unoccupied sites. The lattice is labeled "1D" at the top left and "L=100" at the top right.

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Three horizontal bars representing different lengths. The top bar is 10 units long. The middle bar is 20 units long. The bottom bar is 10 units long, with a double quote mark above the 10th unit.

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